

10/01/01

10-02-09 09/937968.013002  
JC10 Rec'd PCT/PTO 01 OCT 2004FORM PTO-1390  
(REV. 11-2000)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER

TRANSMITTAL LETTER TO THE UNITED STATES  
DESIGNATED/ELECTED OFFICE (DO/EO/US)  
CONCERNING A FILING UNDER 35 U.S.C. 371

MSA 246

U.S. APPLICATION NO. (If known, see 37 CFR 1.5)

09/937968

INTERNATIONAL APPLICATION NO.

PCT/EPO0/02889

INTERNATIONAL FILING DATE

31 MARCH 2000

PRIORITY DATE CLAIMED

31 MARCH 1999

TITLE OF INVENTION

SAFETY DEVICE FOR LIMITING CURRENT AND VOLTAGE

APPLICANT(S) FOR DO/EO/US


WILHELM FEY

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☐ This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (21) indicated below.
4. ☒ The US has been elected by the expiration of 19 months from the priority date (Article 31).
5. ☒ A copy of the International Application as filed (35 U.S.C. 371(c)(2))
  - a. ☐ is attached hereto (required only if not communicated by the International Bureau).
  - b. ☐ has been communicated by the International Bureau.
  - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☒ An English language translation of the International Application as filed (35 U.S.C. 371(c)(2))
  - a. ☒ is attached hereto.
  - b. ☐ has been previously submitted under 35 U.S.C. 154(d)(4).
7. ☐ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))
  - a. ☐ are attached hereto (required only if not communicated by the International Bureau).
  - b. ☐ have been communicated by the International Bureau.
  - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
  - d. ☐ have not been made and will not be made.
8. ☐ An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371 (c)(3)).
9. ☐ An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).
10. ☒ An English language translation of the annexes of the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).

## Items 11 to 20 below concern document(s) or information included:

11. ☒ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
12. ☐ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
13. ☐ A FIRST preliminary amendment.
14. ☐ A SECOND or SUBSEQUENT preliminary amendment.
15. ☐ A substitute specification.
16. ☐ A change of power of attorney and/or address letter.
17. ☐ A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825.
18. ☒ A second copy of the published international application under 35 U.S.C. 154(d)(4).
19. ☐ A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).
20. ☐ Other items or information:

U.S. APPLICATION NO. <b>09/937968</b> INTERNATIONAL APPLICATION NO. <b>PLT/EP00/02889</b>	ATTORNEY'S DOCKET NUMBER <b>MSA246</b>			
21. <input checked="" type="checkbox"/> The following fees are submitted: <b>BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)):</b> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO. .... \$1000.00  International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO ..... \$860.00  International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO ..... \$710.00  International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) ..... \$690.00  International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) ..... \$100.00  <b>ENTER APPROPRIATE BASIC FEE AMOUNT =</b>		<b>CALCULATIONS PTO USE ONLY</b>         \$ 890.00		
Surcharge of \$130.00 for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(e)).		\$		
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE	\$
Total claims	1 - 20 =		x \$18.00	\$
Independent claims	4 - 3 =		x \$80.00	\$
MULTIPLE DEPENDENT CLAIM(S) (if applicable)			+ \$270.00	\$ 280.00
<b>TOTAL OF ABOVE CALCULATIONS =</b>				\$
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27. The fees indicated above are reduced by 1/2.				\$
<b>SUBTOTAL =</b>				\$ 1170.00
Processing fee of \$130.00 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f)).				\$
<b>TOTAL NATIONAL FEE =</b>				\$
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property +				\$
<b>TOTAL FEES ENCLOSED =</b>				\$ 1170.00
<b>Amount to be refunded:</b>				\$
<b>charged:</b>				\$
a. <input type="checkbox"/> A check in the amount of \$ _____ to cover the above fees is enclosed.  b. <input type="checkbox"/> Please charge my Deposit Account No. _____ in the amount of \$ _____ to cover the above fees. A duplicate copy of this sheet is enclosed.  c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. <u>11-0224</u> . A duplicate copy of this sheet is enclosed.  d. <input checked="" type="checkbox"/> Fees are to be charged to a credit card. <b>WARNING:</b> Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.				
<b>NOTE:</b> Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137 (a) or (b)) must be filed and granted to restore the application to pending status.				
SEND ALL CORRESPONDENCE TO.  <div style="display: flex; justify-content: space-between;"> <div style="width: 45%;"> <b>Horst M. Kasper</b>  <b>13 Forest Drive</b>  <b>Warren, N.J. 07059</b> </div> <div style="width: 45%; text-align: right;">           SIGNATURE  <b>HORST M. KASPER</b>          NAME  <b>28,559</b>          REGISTRATION NUMBER       </div> </div>				

410 Rec'd PCT/PTO 0 1 OCT 2001

**Inventor(s):** Wilhelm Fey

**Invention:** SAFETY DEVICE FOR LIMITING CURRENT AND  
VOLTAGE

**Attorney's Docket Number:** MSA246

Horst M. Kasper, his attorney  
13 Forest Drive, Warren, N.J. 07059  
Tel. (908)526 1717; Reg. No. 28559  
Attorney's Docket No.: MSA246

**TRANSLATION OF ANNEXES TO THE INTERNATIONAL  
PRELIMINARY EXAMINATION REPORT dated October 1, 2001**

Application No. : PCT/EP00/02889  
Filing Date : March 31, 2000

7/pst

Safety device for limiting current and voltage

Technical field:

The invention relates to a safety device for limiting current and voltage of an electrical consumer connected downstream of the safety device, wherein the electrical consumer is for example a measurement value transmitter with at least one input connection and one output connection as well as input connection and output connection of a common line, for example a ground line, wherein the safety device includes at least one voltage limiting device and current limiting device such as a Zener barrier, comprising at least one protective device such as a fuse, a voltage limiting device referring to the common line, a current limiting device connected to the output of the voltage limiting device as well as a further protective circuit, which is disposed ahead of the voltage limiting device and the current limiting device.

State-of-the-art:

Many electrical apparatuses or electrical consumers have to be protected against high voltages in order to avoid interruptions, which can lead to damages. In particular, such protective circuits are necessary in regions endangered by explosion.

A circuit arrangement for a safety barrier according to the initially recited kind for limiting of current and voltage at the two wire line running in a region endangered by explosions with two fuses has become known through the European printed patent document EP 0,359,912 A1, said switching arrangement having an input exhibiting two input connections, wherein a voltage source can be connected to the input and an output exhibiting two output connections, wherein the output is connected to a two wire line. A first voltage limiting circuit is connected to the input connections, which first voltage limiting circuit includes a first fuse and a first voltage limiting device. The output of the voltage limiting circuit is connected to a current limiting circuit, wherein at least several parts of the circuit arrangement including the fuse are encased inaccessibly in a casing.

A circuit including a second fuse and a second voltage limiting device exhibiting characteristics similar to those of a Zener diode is disposed between the input and the first voltage limiting circuit. The first voltage limiting circuit is connected on the input side in parallel to a second voltage limiting device and is connected through the second fuse to the input, wherein at least the second fuse is manually accessible. Thus the accessible fuse can also be exchanged in case of a short circuit at the safety device. It is a disadvantage to employ two fuses, of which two fuses one burns through upon responding of the safety device and has to be exchanged manually. An automatic turning on again of the switching device is not possible.

A safety barrier with a barrier input exhibiting two connections, with the barrier output exhibiting two connections and an electronic longitudinal control member with a control input and disposed in a connection between the barrier input and the barrier output has become known from the German patent DE-PS 3622268 (United States patent 4,831,484), wherein the longitudinal control member can be a transistor. A fuse furnished at

the input in connection with voltage limiting Zener diodes serves to switch off the output voltage then, when the voltage at the input of the safety barrier surpasses the blocking voltage of the Zener diodes. Otherwise a current increase would be the consequence, wherein the current increase would be disposed above the current, which current may be delivered by the safety barrier at the output of the safety barrier to the consumer in case of a maximum permissible input voltage. In this case the fuse is released and switches off the output voltage. The maximum output current occurring in case of a short circuit is disposed normally below the release current of the fuse, such that the fuse nominally does not respond in this case. If however the maximum output current is disposed above the release current of the fuse, then device elements of the safety barrier burn through irreparably such that the non-exchangeable fuse cannot perform its purpose.

A shunt diode safety barrier for connection to a voltage source has become known through the European printed patent document EP 0310280 B1, said shunt diode safety barrier including a shunt diode means, a fuse

device component on the voltage supply side of the shunt diode means, a current limiting circuit connected in series with the fuse device component and disposed in sequence with a load for switching and with a heat protective means in order to prevent an overheating of the barrier device components, in case an excessive voltage is applied. The current limiting circuit is connected in series between the fuse device component and the shunt diode means and is disposed such that the fuse device component is protected against applied voltages which are larger than the normal maximum working voltage. The heat protective means within the safety barrier includes a Zener diode, wherein the Zener diode is connected between the fuse device component and the current limiting circuit. This circuit is associated with a disadvantage that the circuit exhibits a high shunt current and thereby a high dissipation loss power. In addition this kind of circuit has a substantial longitudinal voltage drop.

A safety barrier with two transistors for limiting of current and voltage of an electrical consumer to connected downstream from the safety barrier has become known from United States patent 3,818,273, wherein the

electrical consumer has at least one input connection and one output connection, wherein the safety barrier has at least one voltage and current limiting device, such as a Zener barrier, as well as exhibits a current limiting device connected to the output of the Zener barrier. A voltage sensor circuit is provided for voltage detection between the base of the second transistor and a common line. It is disadvantageous that the circuit, which is laid out for several 10 volts, exhibits a substantial shunt current, since a voltage divider out of two resistors is disposed at the input of the circuit, wherein the base of the first transistor is connected between the two resistors through a diode. A continuous shunt current flows through this voltage divider.

A two wire device for protecting a process control device connected downstream of the two wire device has become known from the printed patent document WO 92/02066, wherein the two wire device exhibits a current circuit for protecting against over currents or reverse currents. For this purpose the two wire device has two current sensors, which control circuits with variable impedances, which variable impedances in turn limit

the output currents, which output currents are led thereafter to the process control device.

Finally a switching device for a current limiting for shunting in front of digital long distance apparatuses fed through the subscriber line has become known through the German printed patent document DE 3801250. A field effect transistor is disposed in the longitudinal branch of the circuit arrangement, wherein the passage of the field effect transistor through the gate of the field effect transistor is controlled both by the through control of a transistor depending on the applied supply voltage as well as by the charge of a capacitor.

Technical object:

It is an object of the present invention to furnish a safety device of the recited kind for protecting a voltage and current limiting device, such as a Zener barrier or another circuit to be protected, wherein the safety device is to protect the voltage and current limiting device against destruction

upon over voltage as well as the safety device is to exhibit in particular a low dissipation loss power, wherein voltage losses as well as shunt currents are to be only small.

#### Disclosure of the Invention and of the advantages of the Invention:

This object is achieved by a safety device for limiting of current and voltage of an electrical consumer such as for example a measurement value transmitter connected downstream to the safety device, wherein the electrical consumer is furnished with at least one input connection and one output connection as well as input and output connection of a common line, for example a ground line, wherein the safety device exhibits at least one voltage and current limiting device such as a Zener barrier, comprising at least one protective device such as a fuse, a voltage limiting device referring to the common line, a current limiting device connected to the output of the voltage limiting device as well as a further protective circuit, which further protective circuit is disposed in front of the voltage and current limiting device, wherein the further protective circuit includes a

field effect transistor as a switching and/or regulating transistor, wherein the source drain leg of the switching and/or regulating transistor is disposed between the input connection and the voltage and current limiting device and wherein the gate is connected to the common line through a resistor for feeding in of the control voltage of the field effect transistor, wherein a second transistor is connected at the input connection and at the gate of the switching and/or regulating transistor, wherein the collector of the second transistor is connected to the gate of the switching and/or regulating transistor for influencing the control voltage of the switching and/or regulating transistor and wherein the voltage behind the switching and/or regulating transistor is fed back after the drain of the switching and/or regulating transistor between the output of the further protective circuit through a feedback resistor onto the base of the second transistor, wherein a voltage sensor circuit is disposed between the base of the second transistor and the common line for voltage detection or a longitudinal resistor as a current sensor is disposed between the input connection and the source of the switching and/or regulating transistor for current detection.

The safety device according to the present Invention advantageously serves for turning off over voltages or, respectively, for limiting over voltages as well as for turning off over currents or, respectively, for limiting over currents. The protective circuit advantageously gets by without an exchangeable fuse. Thus it is assured that the inaccessible fuse of the voltage and current limiting device, which can include the Zener barrier, does not be destroyed upon occurrence of an over voltage. Advantageously both the necessary requirements of a safety device as well as the requirements of a service free electronic fuse can be combined.

The safety device exhibits in particular a small dissipation loss power, since the safety device has nearly no shunt current in the region of the operating voltage and only a very small voltage drop through the switching and/or regulating transistor, that is a longitudinal voltage drop. The safety device similarly exhibits a small dissipation loss power in its switched off state and possibly in its down controlled state. The safety device can be

produced at favorable prices with discrete device elements. Further advantageous embodiments result from the sub claims.

Advantageously, three base circuits of the further protective circuit of the safety device closely connected to each other are presented. Either a longitudinal resistor is placed as a current sensor between the input connection and the source of the switching and/or regulating transistor for current detection. The initiation of the switching or regulating is triggered through the resistors R1 through R3 and is performed by the load current at the line point 9, which flows into the voltage and current limiting device. This circuit is to be applied advantageously there, where high load currents are to be avoided.

Alternatively a voltage sensor circuit dispensing the current sensing longitudinal resistor is disposed between the base of the second transistor and the common line for voltage detection. This circuit without current sensing longitudinal resistor mainly serves for switching off the over voltage or, respectively, for limiting the over voltage. The initiation of the

switching off or regulating is triggered through the resistor R5 and the diode D1 and is performed to occur through the input voltage UE. The main advantage of this circuit comprises that the voltage drop of the protective circuit can be maintained extremely small, which causes small dissipation loss powers (compare figure 7).

If the protective circuit is to serve simultaneously both for voltage limitation as well as for current limitation, then both the longitudinal resistor R1 is present as a current sensor as well as the voltage sensor circuit is present as a voltage detector, such that advantageously over voltage switching off or, respectively over voltage limitation and over current switching off or, respectively, over current limitation are combined.

A Zener diode or a Diac diode can serve as a voltage detector in the voltage sensor circuit, wherein a resistor R5 is connected in series to the Zener diode or Diac diode. In case switching off properties of this further protective circuit are desired, then a resistor R2 is placed between the base

of the transistor Q2 and the source of the switching and/or regulating transistor Q1 for reducing the then required feedback current. The size of this resistor determines the required feedback current. The value of the resistor R2 can be disposed between zero and infinity.

The feedback resistor can be replaced by a control or regulating circuit for adjusting the feedback current independent of the output or, respectively, supply voltage, wherein the control or regulating circuit can be for example a constant current circuit, in order to be able to adjust the maximum feedback current independent of the output voltage or, respectively, of the supply voltage.

According to a particular advantageous embodiment of the safety device, the feedback current is adjusted by way of the feedback resistor or the control or regulating circuit such that in case of overload there results a regulating down of the load current to a minimum value and only upon application of a voltage larger than the input nominal voltage, there is performed a switching off of the current into the voltage and current

limiting device, and an automatic switching on again is given upon the following lowering of the supply voltage to the input nominal voltage. This is associated with the advantage that the safety device after its responding or, respectively, after the switching off of the load is capable of automatically switching on again, as soon as the over voltage or, respectively, the over current has been reduced to the input nominal voltage or, respectively, the nominal current. The advantage is presented therewith that the safety device is capable automatically to switch on again after the responding of the safety device or, respectively after the switching off of the load, as soon as the over voltage or, respectively, the over currents return to the input nominal voltage or, respectively, the nominal current.

The resistor can be placed between the base of the transistor Q2 and the source of the switching and/or regulating transistor Q1 for reducing the feedback current in the further protective circuit.

The feedback voltage of the feedback resistor can be both tappable immediately after the drain of the switching and/or regulating transistor as well as at any arbitrary circuit point of the current path between the line points 9 and 16 (figure 1) and can be fed at to the base of the second transistor.

The safety device can for example exhibit a feedback resistor of such size that a return current regulated to a fraction of the load current to be limited upon operation with nominal voltage. The safety device then does not switch off upon input nominal voltage and occurring over current; in case of a presence of an over voltage the feedback current is increased by the ratio of the input voltage to

Protection of the gate source leg is disposed. Alternatively a Zener diode is connected in series to the resistor R4 for reducing the gate control voltage. Depending on this elected field effect transistor, these Zener diodes protect against too large control voltages at the gate. The Zener

diodes can also be an integral component of the switching and/or regulating transistor.

The safety device or, respectively protective circuit can exhibit a reset device such as a key, for switching on again the further protective circuit in case the voltage and current limiting device should trigger. This can be of advantage in particular then where the feedback is adjusted such that the protective circuit separates permanently the downstream connected circuit to be protected or, respectively, load from the supply voltage upon responding.

Furthermore a bipolar transistor can be employed as a switching and/or regulating transistor instead of the field effect transistor in the safety device or, respectively, protective circuit, wherein the collector emitter leg is disposed between the input connection and the output connection of the further protective circuit - relative to figure 1 at the knot 9 - and wherein the base is connected to the common line through a resistor for feeding of the base control voltage.

Any arbitrary such device can be disposed within the safety device as a voltage and current limiting device, for example a Zener barrier in a known or alternate embodiment, as well as the protective device can be arbitrary, for example a fuse. If the safety device is to be employed in a region endangered by explosion, then a fuse together with usually a Zener barrier is combined in the voltage and current limiting device of the safety device.

Short description of the drawings, where there is shown:

Figure 1 a circuit diagram of a safety device for over voltage switching off or, respectively over voltage limitation for protecting the voltage and current limiting device as well as a consumer connected downstream,

Figure 2 a circuit diagram of a further safety device with a current sensing resistor preferably for over current switching off or, respectively over

current limitation or other circuit to be protected or of the consumer connected downstream,

Figure 3 a circuit diagram of a safety device with the combination of over current switching off or, respectively, over current limitation and over voltage switching off or, respectively, over voltage limitation, wherein additionally a Zener diode is disposed in the feed line of the gate of the field effect transistor,

Figure 4 the circuit diagram of safety device according to figure 3 with a consumer connected downstream, wherein the feedback resistor is connected after the safety device,

Figure 5 a further technical embodiment of the safety device,

Figure 6 voltage courses  $U_{9,11}$  and  $U_E$  upon triggering of the safety barrier according to figure 2 at different values of the feedback resistor, and

Figure 7 voltage courses U9,11 and UE upon triggering of the safety barrier according to figure 1 at different values of the feedback resistor.

Paths for performing the Invention:

Figure 1 shows a circuit diagram of a safety device 19, wherein the circuit diagram serves for voltage detection and preferably represents an over voltage switching off or, respectively, and over voltage limitation for protecting the safety device 19 itself as well as the electrical consumer 15 to be protected and connected downstream. The safety device 19, which can in principle be inserted into a two wire line, has at least two input connections 8,10 and at least two output connections 16 and 17, wherein the input connection 10 and the output connection 17 belong to a common line 12, for example a ground line, or, respectively can coincide. An electrical consumer 15 is connectable to the output connections 16, 17.

The safety device 19 surrounded by dashed lines comprises in principle a safety fuse F1 disposed in a line 8 -- 9 -- 16, wherein the safety fuse F1 is preferably a fusible fuse, as well as a voltage limiting device referring from a knot 18 of the line 8 -- 9 -- 16 to the common line 12, which voltage limiting device is symbolized by the Zener diode D3; it is also possible to employ a plurality of diodes disposed in parallel or other known barriers such as Zener barriers. A current limiting device follows to the connection knot 18 in the line 8 -- 9 -- 16 of the first voltage limiting device, wherein the voltage limiting device is disposed in series with the safety fuse F1 and is symbolized by the resistor R6. Preferably a resistor R7 can be connected in series to the fuse F1 in front of the connection knot in the line 8 -- 9 -- 16 of the first voltage limiting device. This voltage current limiting device is fully surrounded with edges in figure 1 and designated with the reference character 14.

A further second protective circuit 20 is disposed in front of the safety fuse F1, wherein the device components of the further second protective circuit 20 are disposed in part parallel to the input connections 8,10 and partially

in series with the safety fuse F1 within the line 8 -- 9 -- 16 or also 10 -- 17 and which further second protective circuit 20 represents also a voltage and/or current limiting circuit. The voltage and/or current limiting circuit in principle comprises a field effect transistor Q1 as a switching and/or regulating transistor, wherein the field effect transistor Q1 is operated as a longitudinal control member in the figures 1,2 or 3 as a switch and/or regulating transistor. For this purpose the field effect transistor Q1 with its source drain leg is disposed longitudinally between the input connector 8 and the knot 9 and in front of the safety fuse F1, wherein the source is connected to the input connector 8 and the drain is connected to the knot 9. The gate G of the switching transistor Q1 is connected to the common line 12 through a resistor R4 for feeding of the control voltage.

A second transistor Q2 is disposed in front of the source gate leg of the field effect transistor Q1, wherein the output of the second transistor Q2, here the collector Q23 is connected to the gate G of the field effect transistor Q1 for influencing the control voltage of the field effect transistor Q1. The emitter Q21 of the transistor Q2 is connected to the

input connector 8. The voltage or, respectively, the current after the source drain leg of the field effect transistor Q1 is fed back to the base Q22 of the second transistor Q2 through the feedback resistor R3 at the knot 9 for controlling the second transistor Q2.

A Zener diode D1 is connected with its anode to the line 12 between the base Q22 of the transistor Q2 and the common line 12, wherein a resistor R5 is disposed in series with the Zener diode D1, wherein the resistor R5 can be optional. A resistor R2 can be disposed on the side of the source S of the field effect transistor Q1 and the base of Q2, wherein the dimensioning of the resistor R2 can be selected such that the resistor R2 serves for reducing the necessary feedback current through the feedback resistor R3.

The triggering of this voltage limiting protective circuit is performed directly by an over voltage, wherein the current from the beginning cannot rise impermissibly in the following voltage and current limiting device D3, R6 with preferably inaccessible fusible fuse F1. Thus the switching off or

controlling down through the Zener diode D1 and the resistor R5 is directly initiated through a supply voltage too high. The initiation of the switching off or of the controlling down is thus performed exclusively through the input voltage UE through the inputs 8,10. The main advantage comprises that the voltage drop and thereby the dissipation loss power of the protective circuit can be maintained extremely small. For example the following values are advantageous: at  $R_{ON} = 0.2 \text{ Ohm}$  and  $I_a = 100 \text{ mA}$  there results a  $V_{Rest} = 20 \text{ mV}$ .

A circuit diagram of a safety device serving for current limiting and a current switch off or, respectively, current limitation for protecting the safety device itself as well as a downstream following electrical consumer 15 to be protected are illustrated in figure 2. A resistor R1 is likewise connected to the input connector 8, wherein the second end of the resistor R1 is connected to the source S of the field effect transistor Q1. This resistor R1 serves as a current sensor for recognizing of impermissibly high currents. The resistor R2 can be similarly present between R1 on the side of the source S and the base of Q2, wherein the resistor R2 serves here

for reducing the necessary feedback current through the feedback resistor R3.

The circuit is constructed in standard operations such that the field effect transistor Q1 receives a control voltage from the supply voltage through the resistor R4 and is maintained in an on-state, such that the drain current flows through the current sensor resistor R1 and the switching transistor Q1. Nearly no control current flows in this state and thus also no shunt current flows into the gate, wherein the shunt current could falsify the measurement value of the current of a possible measurement section. The current sensing resistor R1 controls the base Q22 of the transistor Q2 through the resistor R3, wherein the transistor Q2 is blocked in standard operation.

If the current in the resistor R1 rises to a value above the control voltage UBE of Q2, for example to 0.6 volts, -- for example the shunt current rises in the voltage and current limiting device 7 upon an over voltage -- then additionally a correspondingly rising UBE-voltage is fed back to the base

Q22 of the transistor Q2 through the feedback resistor R3 such that the transistor Q2 becomes conductive. The control voltage of the gate G of the field effect transistor Q1 drops thereby such that the drain current is switched off or, respectively, controlled down and thereby the output current of the protective circuit is switched off or, respectively, controlled down and cannot rise further (constant current). Thus a time based action behavior or flip behavior and regulating behavior occurs depending on the dimensioning of the feedback resistor R3, wherefore the properties of the protective circuit as an automatic controller or as a switch are set by R3.

A small holding current, that is a sensor current or a residual current, flows in the switched off state of the further protective circuit through the feedback resistor R3 and the resistor R2 such present to the outputs 9,11. This residual current can be easily received by the electronics to be protected since these resistors can be laid out correspondingly large for example by a proper current reception of the electronics or by a Zener diode.

It is to be noted in connection with the establishment of the further protective circuit according to figure 2 that the voltage and current limiting device, for example a Zener barrier, represents a voltage depending load, that is an over current is caused immediately by an over voltage at the voltage and current limiting device and only thereby the switching off of the further protective circuit is initiated. A short circuit after the safety device or, respectively, the voltage and current limiting device or, respectively, the Zener barrier is not to be taken into consideration in dimensioning since the fuse F1 is not permitted to trigger the safety device of the previous constructions. The switch off current of the further protective circuit is laid out exclusively for protecting the fuse F1 within the voltage and current limiting device 7, 13, 14.

Additionally constructions of the voltage and current limiting device are possible beyond those recited as have been avoided up to now in the state-of-the-art. The current limiting circuit or, respectively, the resistor R6 has to be dimensioned such that the non-exchangeable fuse F1 is not destroyed upon short circuit at the output. The current limiting circuit or,

respectively the resistor R6 of the voltage and current limiting device 7, 13, 14 can be exclusively dimensioned according to EX-conditions since now an additional current protection is furnished for the fuse F1. For example a lower resistor R6 can furnish a larger output power as previously without that simultaneously the fuse F1 and the Zener diode D3 or several such diodes within the voltage and current limiting device 7,13, 14 have to be reinforced, which means a larger deliverable power in standard operation. In particular such improved EX-conditions are advantageous when a in particular a nonlinear load 15 is connected to the safety device.

Figure 3 shows a safety device in combination with over currents switching off or, respectively, over current limitation and over voltage switching off or, respectively, over voltage limitation of figures 1 to 4 protecting the voltage current limiting device 7 as well as the load 15 connected downstream. The current sensing resistor R1 and the Zener diode D1 of figures 1 and 2 are present such that the functional features of the figures 1 and 2 are present together. In addition a Zener diode D4,

which is optional, is disposed here in series with the resistor R4 in the gate feed line of the field effect transistor Q1.

Furthermore a Zener diode D2 is disposed between the gate and the source of the field effect transistor Q1 and parallel to the gate G and the source S of the field effect transistor for protecting the gate source leg in figure 3, wherein the Zener diode D2 can also be an integral component of the field effect transistor Q1.

Figure 4 shows the circuit diagram of the safety device according to figure 3 for the protection of the voltage and current limiting device 13 as well as of the consumer 15 connected downstream. The feedback resistor R3 is connected to the output 16 of the voltage and current limiting device 13 only after the voltage and current limiting device 13.

Figure 5 shows a further technical embodiment of the protective circuit, wherein the diode D2 is present without the diode D4 parallel to the source

gate leg of this switching and/or regulating transistor Q1, otherwise however the protective circuit corresponds to that shown in figure 3.

The feedback resistor R3 can be replaced by a control or automatic control circuit, wherein the control or automatic control circuit can also be a constant current circuit independent of the output or, respectively, supply voltage for the adjustment of the feedback current.

The feedback current can be adjusted by way of the feedback resistor R3 or of the control or regulating circuit such that a controlling down of the load current to a minimal value results upon overload and only upon application of a voltage larger than the nominal voltage, there is performed a switching off of the load current and thus an automatic switching on again is given during a following lowering of the supply voltage to nominal voltage.

Certain dimension conditions have to be maintained for the feedback resistor R3 in order for the further protective circuit 20 to again automatically switch on after responding or, respectively, triggering. The

lower limit for the dimension of the resistor R3 is given by having the voltage drop over the resistor R2 within the voltage divider R2, R3 remains smaller as the UBE of the transistor Q2, where the transistor Q2 otherwise would open. The upper limit for the dimensioning of the feedback resistor R3 is arbitrarily high depending on the specific application, which means that the feedback resistor R3 can approach infinity, wherein a constant current behavior would set in in this case.

Various illustrating pictures a) through e) are shown in figure 6, wherein this figure 6 refers to the safety device of figure 2. In each case the voltage U9-11 as well as the dissipation loss power over the input voltage or, respectively supply voltage course is plotted in the illustrating pictures a) through d); parameter is a changeable value for the feedback resistor R3 upon a certain selected dimensioning of the remaining device components. If the feedback resistor R3 is selected to be below a certain value, then the further circuit cannot any longer automatically switch on after responding which is the case here for example at the value of R3 of 150KOhm. In case of the value of about 250 KOhm or 330 KOhm the further circuit

again automatically switches on after its responding which is shown in the illustrating pictures b) and c). If the resistor R3 is dimensioned beyond a certain limit, then a constant output voltage appears in consequence of the voltage limiting effect of the voltage and current limiting device 7,13, 14, which is then shown in the illustrating picture d). However here the dissipation loss power of the remaining circuit increases over proportionally.

Figure 7 shows various illustrating pictures a) through e) similar to figure 6, wherein the illustrating pictures a) through e) refer to the safety device of figure 1. The voltage courses U9-11 as well as the dissipation loss powers are shown over the input voltage or, respectively, supply voltage course upon triggering of the safety device at different values of the feedback resistor. The safety device does not switch on any longer after its triggering in case the voltage has dropped to the input nominal voltage UENEN in case of small values of the feedback resistor for example 150 kOhm at otherwise determined selected dimensioning of the remaining device components. The safety device automatically and highly

advantageously switches on again at higher values, for example starting at  $R_3 = 250 \text{ Ohm}$  in contrast, where the triggering and endangering voltage has dropped to the input nominal voltage  $U_{ENEN}$ . This is also a situation in case of very large values of  $R_3$ . It is further recognizable from the illustrating pictures that in fact the dissipation loss power of the safety device is extremely small in all cases to be considered.

In the following there is provided an example for a 'coarse' dimensioning (fine dimensioning is performed with a simulator program) of the resistor's  $R_1$ ,  $R_2$  and  $R_3$  for adapting the current limiter flip-flop, wherein here the variant switching off is considered (not flipping):

$$U_{BEQ2} = (U_E) * (R_1 + R_2) / (R_1 + R_2 + R_3) \Rightarrow R_3 = ((U_E / U_{BEQ2}) - 1) * (R_1 + R_2)$$

Defined  $I_{\max}$  in  $Q_1 = 50 \text{ mA}$

$$R_1 = 0.5 \text{ V} / 50 \text{ mA} = 10 \text{ Ohm}$$

\*  $U_{BEQ2}$  assumed to be 0.5 V

For a desire to re-switching on the short circuit current is fixed to about 10 percent of  $I_{max}$  for the nominal input voltage  $U_{ENEN}$ :

$$U_{R1} = 10 \text{ Ohm} * 5\text{mA} = 50 \text{ mV}$$

$$U_{R2} = 0.5 \text{ V} - 0.05 \text{ V} = 0.45 \text{ V}$$

Definition of the shunt current by  $R_3$  about  $R_2 = 30 \text{ microAmp}$ .

$$R_2 = 0.45 \text{ V} / 30 \text{ microAmp} = 15 \text{ KOhm}$$

$$R_2 = ((U_E / U_{BEQ2}) - 1) * (R_1 + R_2) = (8V^{**} / 0.5V - 1) * (10 \text{ Ohm} + 15 \text{ KOhm}) = 225 \text{ KOhm}$$

\*\*assumed f. Ex

### Commercial applicability:

The subject matter of the present invention is commercially applicable in particular for explosion protected rooms as a safety device as well as the further protective circuit also always there, where an electrical device is to be protected against over voltage or an over current. The further protective circuit alone can advantageously be employed as an electrical or, respectively, electronic shunt fuse, where the further protective device protects downstream electrical apparatus against impermissible voltages and currents upon occurrence of over voltages or over currents; the connected apparatuses are thus also not damaged in case of an inadvertent connection to supply voltages which are too large.

### Mr. preference numerals:

Q1 field effect transistor

Q2 transistor

Q21 emitter of transistor Q2

Q22 base of transistor Q2

Q23 collector of transistor Q2

R1, R2, R3, R4, R5, R6, R7 resistors

D1, D2, D3, D4 diodes

F1 fusible fuse

7,13, 14 voltage and current limiting device, four example Zener barrier

8,10 input connections of the safety device

9,11 outputs or, respectively, knots of the further protective device

12 common line, as ground line

15 consumer or, respectively, load

16, 17 output connectors of the safety device

18 knots

19 safety device

20 further protective circuit

D drain of Q1

S source of Q1

G gate of Q1

$U_E$  input voltage

$U_A$  output voltage

$U_{ENEN}$  input nominal voltage

**Patent claims:**

1. Safety device (19) for limiting of current and voltage of an electrical consumer (15) connected downstream to the safety device (19), for example of a measurement value transmitter, with at least one input connector (8) and one output connector (16) as well as input connector and output connector (10, 17) of a common line (12), for example a ground line, wherein the safety device (19) includes at least voltage and current limiting device (7,13, 14), such as a Zener barrier and comprising at least one protective device (F1) as a fusible fuse, a voltage limiting device (D3) referring to the common line (12), a current limiting device (R6) connected to the output of the voltage limiting device (D3) as well as a further protective circuit (20), which further protective circuit (20) is disposed in front of the voltage and current limiting device (7,13, 14), wherein the further protective circuit (20) exhibits a field effect transistor (Q1) as a switching and/or regulating transistor, wherein the source drain leg (S-D) of the field effect transistor (Q1) is disposed between the input connector (8) and the voltage and current limiting device (7,13, 14) and

wherein the gate (G) is connected to the common line (12) through a resistor (R4) for feeding in the control voltage of the field effect transistor (Q1), wherein a second transistor (Q2) is connected to the input connector (8) and to the gate (G) of the switching and/or regulating transistor (Q1), wherein the collector (Q23) is connected to the gate (G) of the switching and/or regulating transistor (Q1) for influencing the control voltage of the switching and/or regulating transistor (Q1), and wherein the voltage (U9,11) is fed back to the base (Q22) of the second transistor (Q2) over a feedback resistor (R3) between the outputs (9,11) of the further protective circuit (20) and after the switching and/or regulating transistor (Q1) and after the drain (D) of the switching and/or regulating transistor (Q1), wherein a voltage sensor circuit (D1,R5) is disposed between the base (Q22) of the second transistor (Q2) and the common line (12) for voltage detection, or

a longitudinal resistor (R1) as a current sensor is disposed between the input connector (8) and the source (S) of the switching and/or regulating transistor (Q1) for current capturing.

2. Safety device (19) according to claim 1 characterized in that the longitudinal resistor (R1) as a current sensor and the voltage sensor circuit (D1,R5) are present simultaneously both for voltage detection as well as for current limitation.

3. Safety device (19) according to claim 1 or 2 characterized in that the voltage sensor circuit (D1,R5) comprises a Zener diode or Diac diode (D1) and a resistor (R5) connected in series.

4. Safety device (19) according to claim 1 characterized in that the feedback current is adjusted by way of the feedback resistor (R3) or the control and regulating circuit such that in case of over load there results a regulating down of the load current to a minimum value and a switching off of the current in the voltage and current limiting device (7,13, 14) is performed only upon application of a voltage (U8-10) larger than the input nominal voltage (U<sub>EN</sub>) and wherein an automatic switching on again is

given upon following lowering of the supply voltage ( $U_E$ ) to the input nominal voltage ( $U_{EN}$ ).

5. Safety device (19) according to claim 1 characterized in that a resistor ( $R_2$ ) is disposed between the base ( $Q_{22}$ ) of the transistor ( $Q_2$ ) and the source ( $S$ ) of the switching and regulating transistor ( $Q_1$ ) in the further protective circuit (20) for reducing the feedback current.

6. Safety device (19) according to one of the preceding claims, characterized in that the reference voltage or, respectively, the feedback voltage ( $U_{9-11;UA}$ ) of the feedback resistor ( $R_3$ ) is tappable both immediately after the drain ( $D$ ) of the switching and/or regulating transistor ( $Q_1$ ) as well as at any arbitrary circuit point of the current path between the line points 9 and 16 and that the reference voltage or, respectively, the feedback voltage ( $U_{9-11;UA}$ ) of the feedback resistor ( $R_3$ ) is fed back to the base ( $Q_{22}$ ) of the second transistor ( $Q_2$ ).

7. Safety device (19) according to one of the preceding claims characterized in that a Zener diode (D2) is disposed between the gate (G) and the source (S) of the switching and/or regulating transistor (Q1) parallel to the gate (G) and to the source (S) of the switching and/or regulating transistor (Q1) for protecting the gate source leg (G-S).

8. Safety device (19) according to one of the preceding claims characterized in that a Zener diode (D4) is connected in series with the resistor (R4) for reducing the gate control voltage of the switching and/or regulating transistor (Q1).

9. Safety device (19) according to claims 7 and/or 8 characterized in that the Zener diodes D2 and/or D4 are integral components of the switching and/or regulating transistor (Q1).

10. Safety device (19) according to claim 1 characterized in that the feedback resistor (R3) is replaced by a control or regulating circuit for

adjusting the feedback current independent of the output voltage or, respectively, of the supply voltage.

11. Safety device (19) according to claim 10 characterized in that the control or regulating circuit is a constant current circuit.

12. Safety device (19) according to one of the preceding claims characterized in that the safety device (19) includes a reset device, for example a key, for switching on again in the further protective circuit (20) after triggering of the switching off of the current in the voltage and current limiting device (7,13, 14).

13. Safety device (19) according to claim 1 characterized in that the second transistor (Q2) is an electronic relay or field effect transistor or a thyristor.

14. Safety device (19) according to claim 1 characterized in that a bipolar transistor or an electronic relay are employed instead of the field effect transistor.

**PCT**  
WELTORGANISATION FÜR GEISTIGES EIGENTUM  
Internationales Büro  
**INTERNATIONALE ANMELDUNG VERÖFFENTLICHT NACH DEM VERTRAG ÜBER DIE  
INTERNATIONALE ZUSAMMENARBEIT AUF DEM GEBIET DES PATENTWESENS (PCT)**



<p>(51) Internationale Patentklassifikation 7 : <b>H02H 9/02</b></p>	<p><b>A1</b></p>	<p>(11) Internationale Veröffentlichungsnummer: <b>WO 00/62394</b></p> <p>(43) Internationales Veröffentlichungsdatum: 19. Oktober 2000 (19.10.00)</p>											
<table style="width: 100%;"> <tr> <td style="width: 50%; vertical-align: top;"> <p>(21) Internationales Aktenzeichen: <b>PCT/EP00/02889</b></p> <p>(22) Internationales Anmeldedatum: 31. März 2000 (31.03.00)</p> <p>(30) Prioritätsdaten:</p> <table style="width: 100%;"> <tr> <td style="width: 33%;">199 14 549.0</td> <td style="width: 33%;">31. März 1999 (31.03.99)</td> <td style="width: 33%;">DE</td> </tr> <tr> <td>199 24 746.3</td> <td>31. Mai 1999 (31.05.99)</td> <td>DE</td> </tr> <tr> <td>199 31 721.6</td> <td>8. Juli 1999 (08.07.99)</td> <td>DE</td> </tr> </table> <p>(71) Anmelder (für alle Bestimmungsstaaten ausser US): PEP- PERL + FUCHS GMBH (DE/DE); Königsberger Allee 87, D-68307 Mannheim (DE).</p> <p>(72) Erfinder; und</p> <p>(75) Erfinder/Anmelder (nur für US): FEY, Wilhelm (DE/DE); Von Brühl-Strasse 22, D-67246 Dirmstein (DE).</p> <p>(74) Anwalt: MIERSWA, Klaus; Friedrichstrasse 171, D-68199 Mannheim (DE).</p> </td> <td style="width: 50%; vertical-align: top;"> <p>(81) Bestimmungsstaaten: AU, BA, BG, BR, BY, CA, CN, CU, CZ, DE, EE, HU, ID, IL, IN, IS, JP, KR, LT, LV, MA, MD, MK, MX, NO, NZ, PL, RO, RU, SG, SI, SK, TR, UA, US, YU, ZA, ARIPO Patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), eurasisches Patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), europäisches Patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI Patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).</p> <p><b>Veröffentlicht</b> <i>Mit internationalem Recherchenbericht. Vor Ablauf der für Änderungen der Ansprüche zugelassenen Frist; Veröffentlichung wird wiederholt falls Änderungen eintreffen.</i></p> </td> </tr> </table>			<p>(21) Internationales Aktenzeichen: <b>PCT/EP00/02889</b></p> <p>(22) Internationales Anmeldedatum: 31. März 2000 (31.03.00)</p> <p>(30) Prioritätsdaten:</p> <table style="width: 100%;"> <tr> <td style="width: 33%;">199 14 549.0</td> <td style="width: 33%;">31. März 1999 (31.03.99)</td> <td style="width: 33%;">DE</td> </tr> <tr> <td>199 24 746.3</td> <td>31. Mai 1999 (31.05.99)</td> <td>DE</td> </tr> <tr> <td>199 31 721.6</td> <td>8. Juli 1999 (08.07.99)</td> <td>DE</td> </tr> </table> <p>(71) Anmelder (für alle Bestimmungsstaaten ausser US): PEP- PERL + FUCHS GMBH (DE/DE); Königsberger Allee 87, D-68307 Mannheim (DE).</p> <p>(72) Erfinder; und</p> <p>(75) Erfinder/Anmelder (nur für US): FEY, Wilhelm (DE/DE); Von Brühl-Strasse 22, D-67246 Dirmstein (DE).</p> <p>(74) Anwalt: MIERSWA, Klaus; Friedrichstrasse 171, D-68199 Mannheim (DE).</p>	199 14 549.0	31. März 1999 (31.03.99)	DE	199 24 746.3	31. Mai 1999 (31.05.99)	DE	199 31 721.6	8. Juli 1999 (08.07.99)	DE	<p>(81) Bestimmungsstaaten: AU, BA, BG, BR, BY, CA, CN, CU, CZ, DE, EE, HU, ID, IL, IN, IS, JP, KR, LT, LV, MA, MD, MK, MX, NO, NZ, PL, RO, RU, SG, SI, SK, TR, UA, US, YU, ZA, ARIPO Patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), eurasisches Patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), europäisches Patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI Patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).</p> <p><b>Veröffentlicht</b> <i>Mit internationalem Recherchenbericht. Vor Ablauf der für Änderungen der Ansprüche zugelassenen Frist; Veröffentlichung wird wiederholt falls Änderungen eintreffen.</i></p>
<p>(21) Internationales Aktenzeichen: <b>PCT/EP00/02889</b></p> <p>(22) Internationales Anmeldedatum: 31. März 2000 (31.03.00)</p> <p>(30) Prioritätsdaten:</p> <table style="width: 100%;"> <tr> <td style="width: 33%;">199 14 549.0</td> <td style="width: 33%;">31. März 1999 (31.03.99)</td> <td style="width: 33%;">DE</td> </tr> <tr> <td>199 24 746.3</td> <td>31. Mai 1999 (31.05.99)</td> <td>DE</td> </tr> <tr> <td>199 31 721.6</td> <td>8. Juli 1999 (08.07.99)</td> <td>DE</td> </tr> </table> <p>(71) Anmelder (für alle Bestimmungsstaaten ausser US): PEP- PERL + FUCHS GMBH (DE/DE); Königsberger Allee 87, D-68307 Mannheim (DE).</p> <p>(72) Erfinder; und</p> <p>(75) Erfinder/Anmelder (nur für US): FEY, Wilhelm (DE/DE); Von Brühl-Strasse 22, D-67246 Dirmstein (DE).</p> <p>(74) Anwalt: MIERSWA, Klaus; Friedrichstrasse 171, D-68199 Mannheim (DE).</p>	199 14 549.0	31. März 1999 (31.03.99)	DE	199 24 746.3	31. Mai 1999 (31.05.99)	DE	199 31 721.6	8. Juli 1999 (08.07.99)	DE	<p>(81) Bestimmungsstaaten: AU, BA, BG, BR, BY, CA, CN, CU, CZ, DE, EE, HU, ID, IL, IN, IS, JP, KR, LT, LV, MA, MD, MK, MX, NO, NZ, PL, RO, RU, SG, SI, SK, TR, UA, US, YU, ZA, ARIPO Patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), eurasisches Patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), europäisches Patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI Patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).</p> <p><b>Veröffentlicht</b> <i>Mit internationalem Recherchenbericht. Vor Ablauf der für Änderungen der Ansprüche zugelassenen Frist; Veröffentlichung wird wiederholt falls Änderungen eintreffen.</i></p>			
199 14 549.0	31. März 1999 (31.03.99)	DE											
199 24 746.3	31. Mai 1999 (31.05.99)	DE											
199 31 721.6	8. Juli 1999 (08.07.99)	DE											
<p>(54) Title: <b>SAFETY BARRIER FOR LIMITING CURRENT AND VOLTAGE</b></p> <p>(54) Bezeichnung: <b>SICHERHEITSBARRIERE ZUM BEGRENZEN VON STROM UND SPANNUNG</b></p>													
<p>(57) Abstract</p> <p>The invention relates to a safety barrier for limiting the current and voltage of an electrical consumer (15) connected downstream thereof. The safety barrier comprises an input terminal (8) and an output terminal (16) and an input and output terminal (10, 17) of a common line (12), and has at least one voltage and current-limiting device (7, 13, 14), which comprises a fuse (F1), a voltage-limiting device (D3) that is associated with the common line (12), a current-limiting device (R6) that is connected to the output of the common line and an additional protection circuit that is located in front of the voltage and current-limiting device (7, 13, 14).</p>													

19/04 '01 DO 23:43 FAX +49 621 856001  
19-04-2001

RAEPA KLAUS MIERSWA  
PCT/EP00/02889

--- EURO PATENTAMT

DRAW

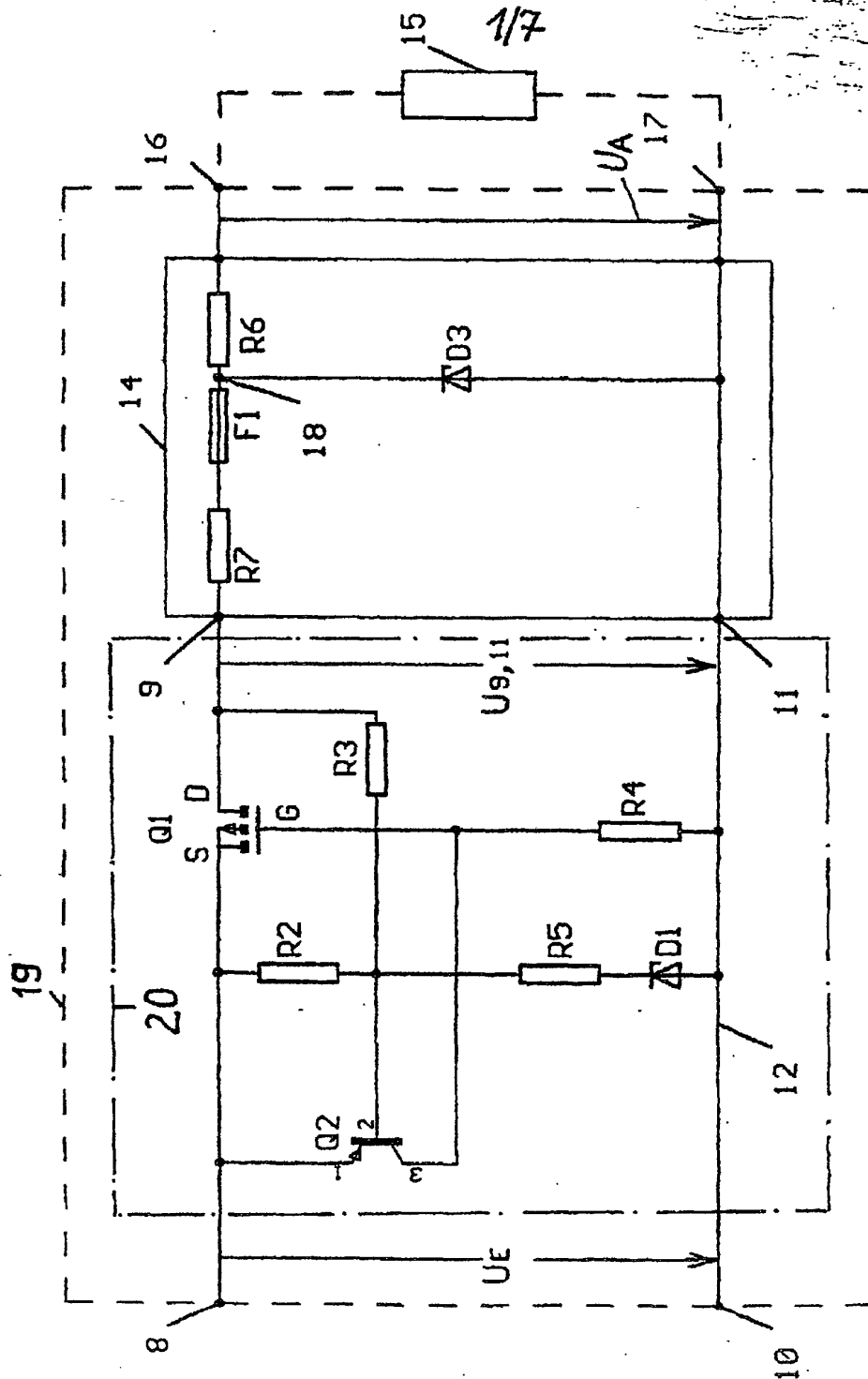


Fig. 1

19/04 '01 DO 23:43 FAX +49 621 856001  
19-04-2001

KAKA FIATIS MIERSWA  
PCT/EP00/02889

EURO PATENTAMT E  
DRAW

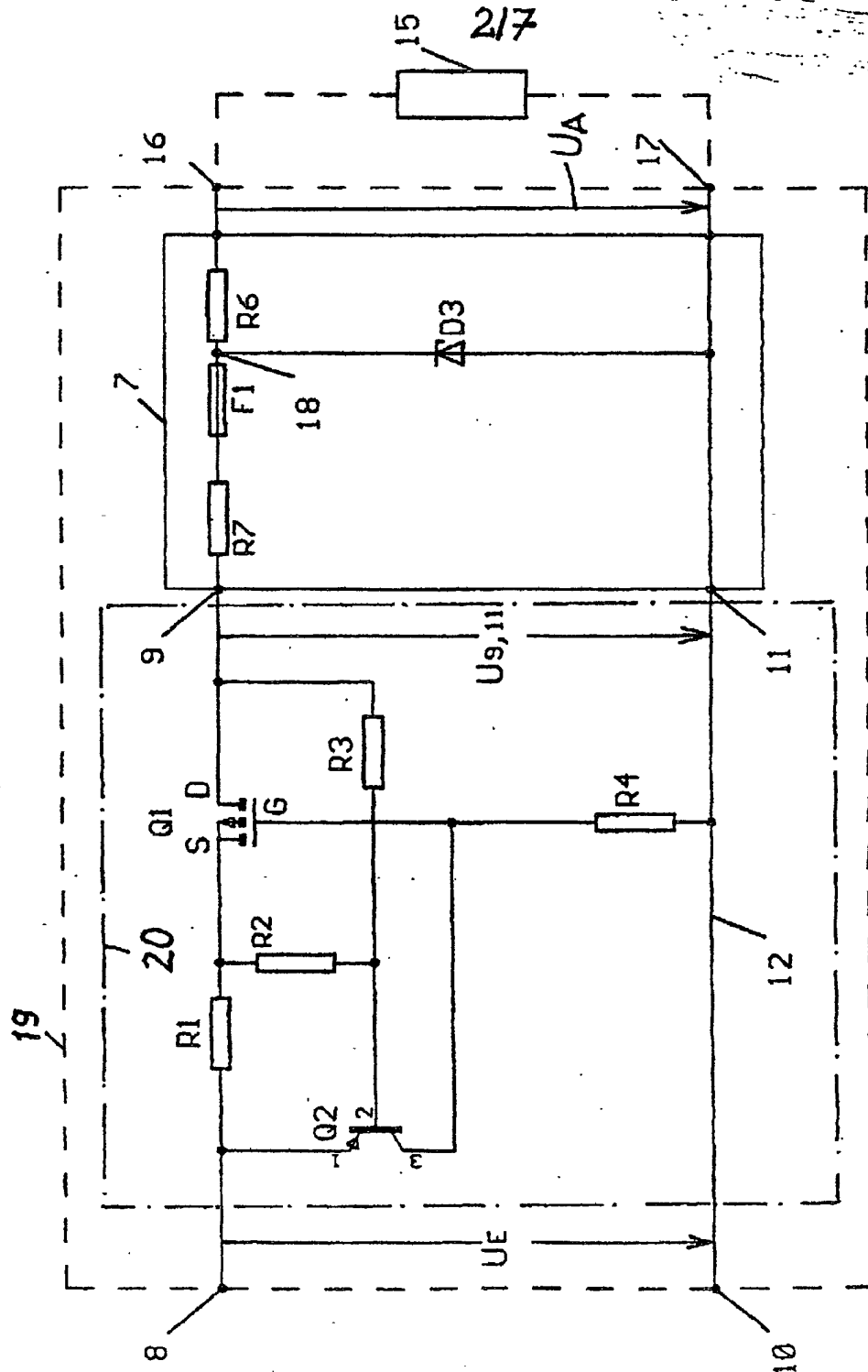


Fig.2

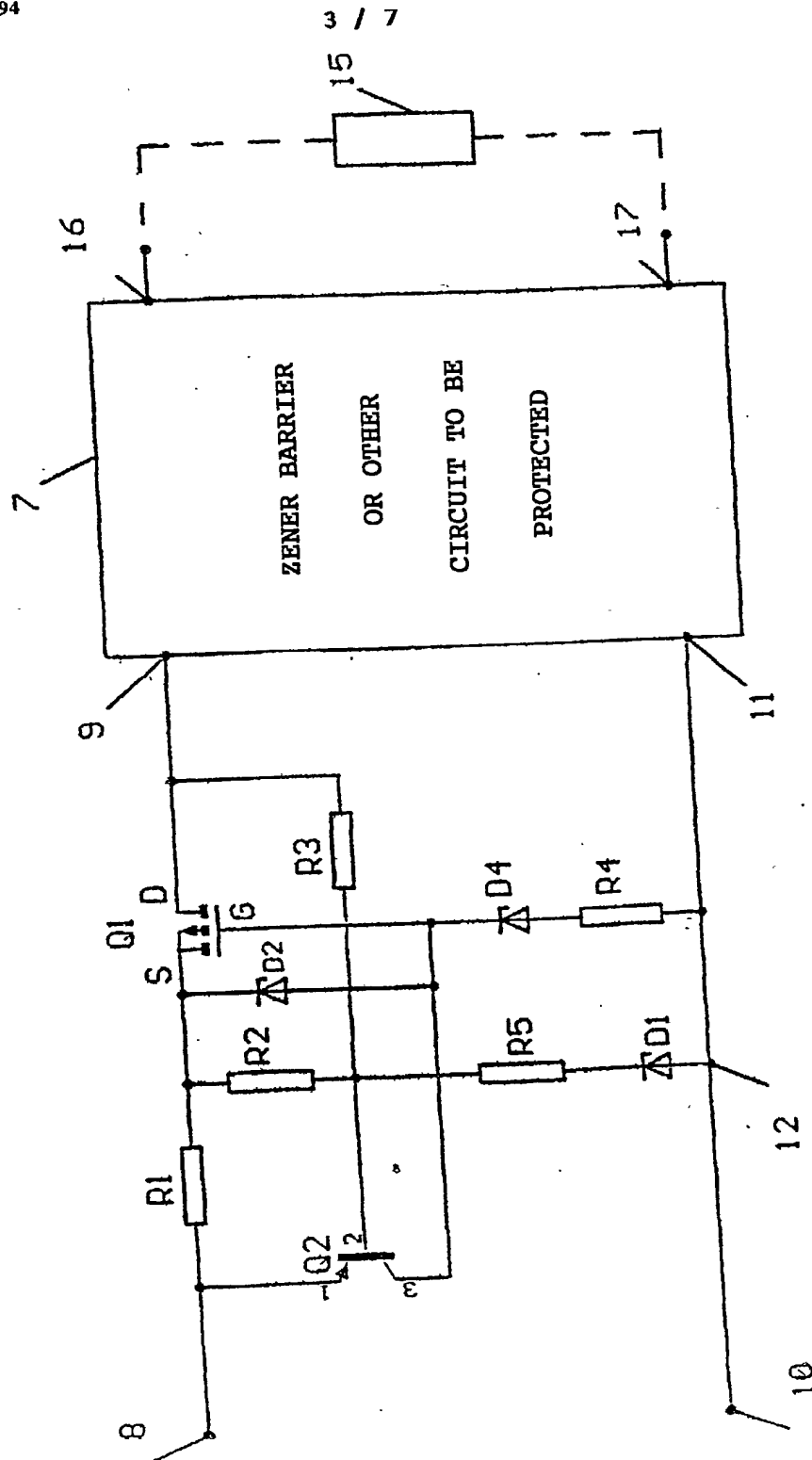


Fig. 3

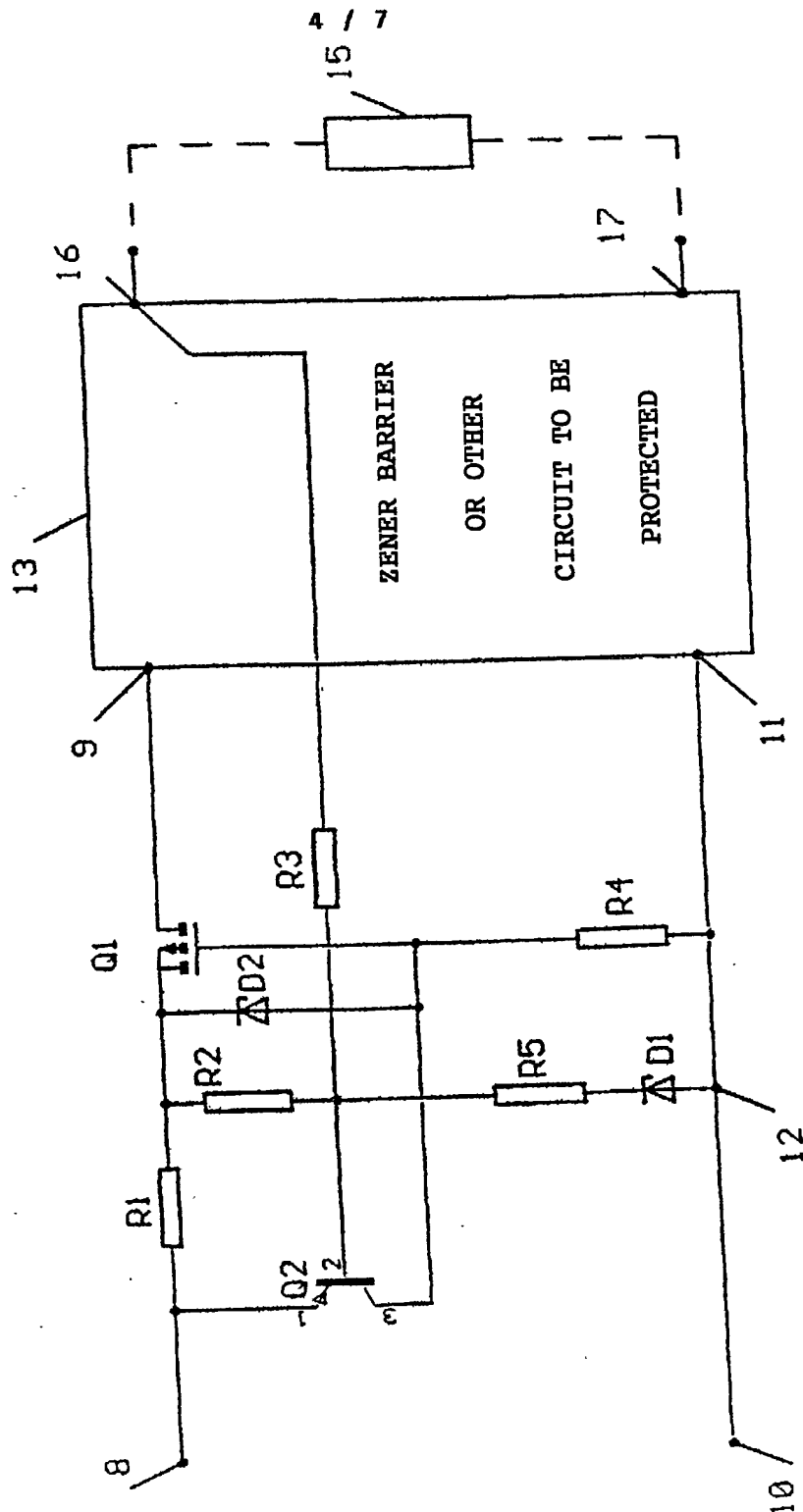
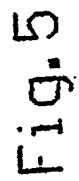


Fig. 4



5  
D  
F

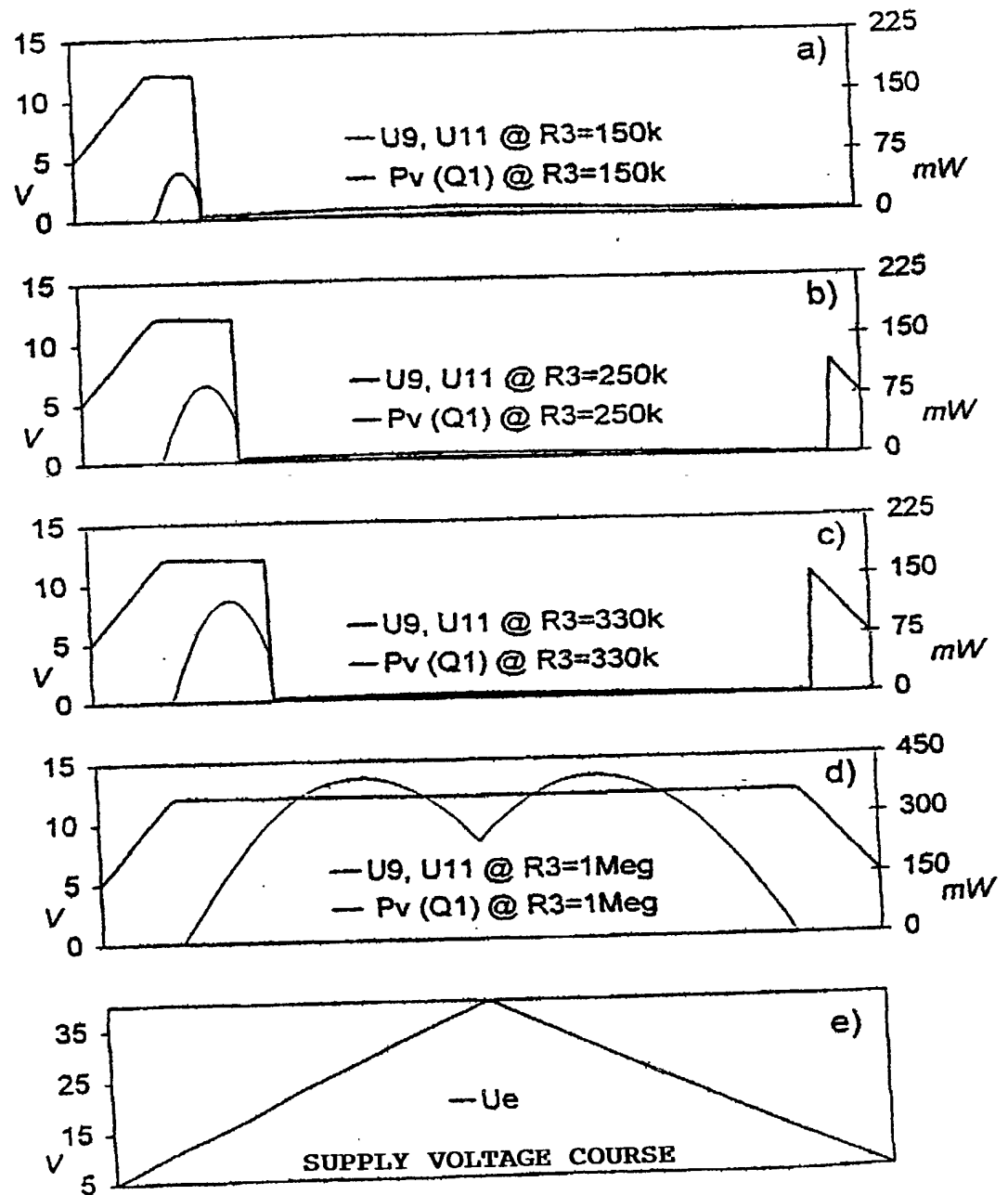


Fig. 6

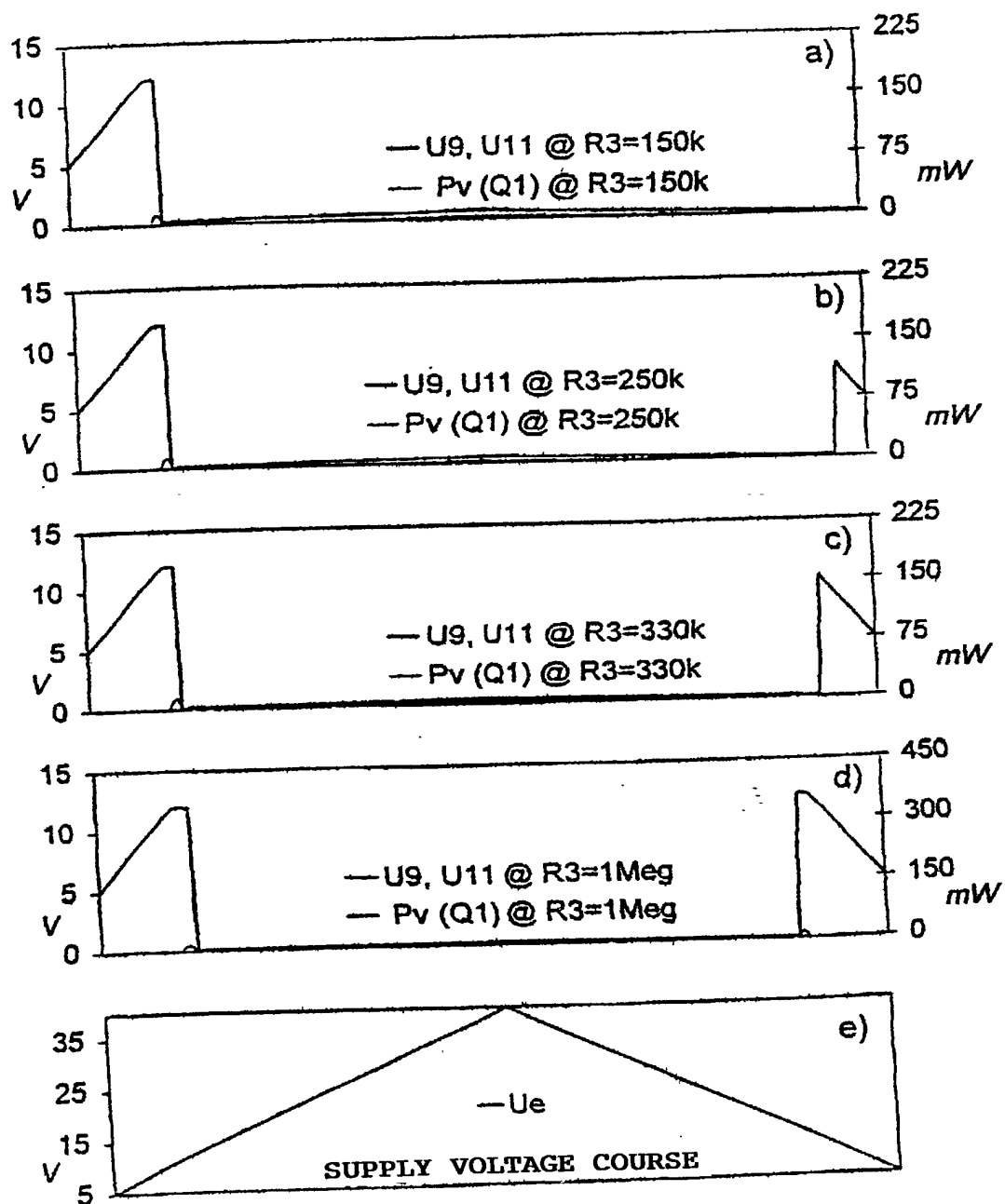


Fig. 7

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wilhelm Fey

Serial No: Art Unit:

Filing Date:

Title: SAFETY BARRIER FOR LIMITING CURRENT AND VOLTAGE

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION  
Erklärung Für Patentanmeldungen Mit Vollmacht  
German Language Declaration Msa246Als nachstehend benannter Erfinder erkläre ich hiermit an  
Eidesstatt:

As a below named inventor, I hereby declare that:

daß mein Wohnsitz, meine Postanschrift und meine Staats-  
angehörigkeit den im Nachstehenden nach meinem Namen aufgeführten  
Angaben entsprechen,My residence, post office address and citizenship are as stated  
below next to my name,daß ich, nach bestem Wissen der ursprüngliche, erste und  
alleinige Erfinder (falls nachstehend nur ein Name angegeben ist)  
oder ein ursprünglicher, erster und Miterfinder (falls  
nachstehend mehrere Namen aufgeführt sind) des Gegenstandes bin,  
für den dieser Antrag gestellt wird und für den ein Patent  
beantragt wird für die Erfindung mit dem Titel:

Sicherheitsbarrriere zum Begrenzen von Strom und Spannung

I believe I am the original, first and sole inventor (if only one  
name is listed below) or an original, first and joint inventor  
(if plural names are listed below) of the subject matter which is  
claimed and for which a patent is sought on the invention  
entitled:

SAFETY BARRIER FOR LIMITING CURRENT AND VOLTAGE

deren Beschreibung (nur eines der nachfolgenden Kästchen ankreuzen)

the specification of which (check only one item below)

< > hier beigefügt ist.  
is attached hereto.

< > am \_\_\_\_\_ als U.S.-Anmeldung, Seriennummer  
eingereicht wurde und am \_\_\_\_\_ abgeändert wurde (falls  
tatsächlich abgeändert).  
was filed as US Application Serial No. \_\_\_\_\_ on  
and was amended on \_\_\_\_\_ (if applicable).

<X> am 31. März 2000 als internationale PCT-Anmeldung, Nummer  
PCT/EP00/02889 eingereicht wurde und am 19. April 2001 unter PCT-  
Artikel 36 abgeändert wurde (falls tatsächlich abgeändert).  
was filed as PCT international application, Number PCT/EP00/02889  
on 31 March 2000 and was amended under PCT Article 36  
on 19 April 2001 (if applicable)

Ich bestätige hiermit, daß ich den Inhalt der obigen  
Patentanmeldung einschließlich der Ansprüche durchgesehen und  
verstanden habe, die eventuell durch einen Zusatzantrag wie oben  
erwähnt abgeändert wurde.

I hereby state that I have reviewed and understand the contents  
of the above-identified specification, including the claims, as  
amended by any amendment referred to above.

Ich erkenne meine Pflicht zur Offenbarung jeglicher Informationen  
an, die zur Prüfung der Patentfähigkeit in Einklang mit Titel 37,  
Bundesgesetzbuch (Code of Federal Regulation), § 1.56 von Belang  
sind.

I acknowledge the duty to disclose information which is material  
to patentability as defined in Title 37, Code of Federal  
Regulations, § 1.56.

Ich beanspruche hiermit ausländische Prioritätsvorteile gemäß  
Abschnitt 35 der Zivilprozeßordnung der Vereinigten Staaten,  
Paragraph 119 jeglicher unten angegebenen Auslandsanmeldung(en)  
für ein Patent oder Erfindersurkunde oder jeglicher  
internationalen PCT-Anmeldung(en), welche mindestens ein Land  
ausser den Vereinigten Staaten benennt, und habe auch jegliche  
Auslandsanmeldung(en) für ein Patent oder Erfindersurkunde oder

jegliche internationale PCT-Anmeldung(en), welche mindestens ein Land ausser den Vereinigten Staaten benennt, nachstehend gekennzeichnet, welche von mir für den gleichen Gegenstand eingereicht wurde und ein Anmeldedatum haben, das vor dem Anmeldedatum der Anmeldung liegt, für die Priorität beansprucht wird.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application(s) for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

**PRIOR FOREIGN /PCT APPLICATION(S) AND ANY PRIORITY CLAIMS UNDER 35 USC 119:**

**FRÜHERE AUSLÄNDISCHE/PCT ANMELDUNG(EN) UND JEGliche PRIORITÄT UNTER 35 USC 119:**

Country (if PCT, indicate PCT) Land (falls PCT, PCT angeben)	Application No. Anmeldungsnummer	Date of Filing (day, month, year) Anmeldedatum (Tag, Monat, Jahr)	Priority Claimed under 35 USC 119 Priorität unter 35 USC 119 beansprucht
Germany	199 14 549.0	31 March 1999	<X>Yes < >No Ja Nein
Germany	199.24 746.3	31 May 1999	
Germany	199 31 721.6	8 July 1999	

Ich beanspruche hiermit gemäß Absatz 35 der Zivilprozeßordnung der Vereinigten Staaten, Paragraph 120, den Vorzug jeglicher unten aufgeführten U.S.-Anmeldung(en) oder die USA benennende internationale(n) PCT-Anmeldung(en) und falls der Gegenstand aus jedem Anspruch dieser Anmeldung nicht in dieser/diesen früheren Patentanmeldung(en) laut dem ersten Paragraphen des Absatzes 35 der Zivilprozeßordnung der Vereinigten Staaten, Paragraph 112 offenbart ist, erkenne ich gemäß Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) meine Pflicht zur Offenbarung von Informationen an, die zwischen dem Anmeldedatum der früheren Anmeldung(en) und dem nationalen oder internationalen PCT Anmeldedatum dieser Anmeldung bekannt geworden sind.

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) or PCT international application(s) designating the United States of America that is/are listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in that/those prior application(s) in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application(s) and the national or PCT international filing date of this application:

**PRIOR U.S. APPLICATIONS OR PCT INTERNATIONAL APPLICATIONS DESIGNATING THE U.S. FOR BENEFIT UNDER 35 USC 120:**

FRÜHERE AMERIKANISCHE ANMELDUNGEN ODER DIE USA BENENNENDE		
INTERNATIONALE PCT-ANMELDUNGEN FÜR VORRECHT UNTER 35 USC 120		
U.S. APPLICATIONS		STATUS (Check one)
U.S. Application No.	U.S. Filing Date	Patented Pending Abandoned
AMERIKANISCHE ANMELDUNGEN		STAND (ein Kästchen ankreuzen)
Seriennummer	Anmeldedatum	Patentiert Anhängig Aufgegeben

< > < > < >

**PCT APPLICATIONS DESIGNATING THE U.S.**

PCT Application Number	PCT Filing Date	U.S.Ser.Nos.
		assigned (if any)

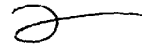
DIE USA BENENNENDE PCT-ANMELDUNGEN		
PCT-Anmelde-	PCT-Anmeldedatum	Zugeteilte Serien-
nummer		nummern (falls zutreffend)

< > < > < >

**VERTRETUNGSVOLLMACHT:** Als benannter Erfinder beauftrage ich hiermit den nachstehend benannten Patentanwalt (oder die nachstehend benannten Patentanwälte) und/oder Patent-Agenten mit der Verfolgung der vorliegenden Patentanmeldung sowie mit der Abwicklung aller damit verbundenen Geschäfte vor dem Patent- und Warenzeichenamt: (Name und Registrationsnummer anführen)

**POWER OF ATTORNEY:** As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (List name and registration number)

Horst M. Kasper (Reg. No. 28,559)  
Richard T. Laughlin (Reg. No. 17,264)



Telefongespräche bitte richten an: (Name und Telefonnummer)  
 Direct Telephone Calls to: (Name and telephone number)

Horst M. Kasper  
 (908) 757-2839

Postanschrift:  
 Send Correspondence to:

13 Forest Drive; Warren, N.J. 07059

Ich erkläre hiermit, daß alle von mir in der vorliegenden Erklärung gemachten Angaben nach meinem besten Wissen und Gewissen der vollen Wahrheit entsprechen, und daß ich diese eidesstattliche Erklärung in Kenntnis dessen abgebe, daß wissentlich und vorsätzlich falsche Angaben gemäß Paragraph 1001, Absatz 18 der Zivilprozeßordnung der Vereinigten Staaten von Amerika mit Geldstrafe belegt und/oder Gefängnis bestraft werden können, und daß derartig wissentlich und vorsätzlich falsche Angaben die Gültigkeit der vorliegenden Patentanmeldung oder eines darauf erteilten Patentbeschlusses gefährden können.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Voller Name des einzigen oder ursprünglichen Erfinders:  
 Full name of sole or first inventor:

Wilhelm Fey

Unterschrift des Erfinders  
 Inventor's signature

*100*  
*Wilhelm Fey*  
 .....

Datum

Date

*27/12/2001*  
 .....

Wohnsitz  
Residence

D-67246 Dirmstein  
Germany  
Staatsangehörigkeit  
Citizenship

DEX

Germany

Postanschrift  
Post Office Address

Von Brühl-Strasse 22  
D-67246 Dirmstein  
Germany

PTO 1391 (10-83)  
\*%pt0:germde(Msa246(October 3, 2001(th